

FIG. 1

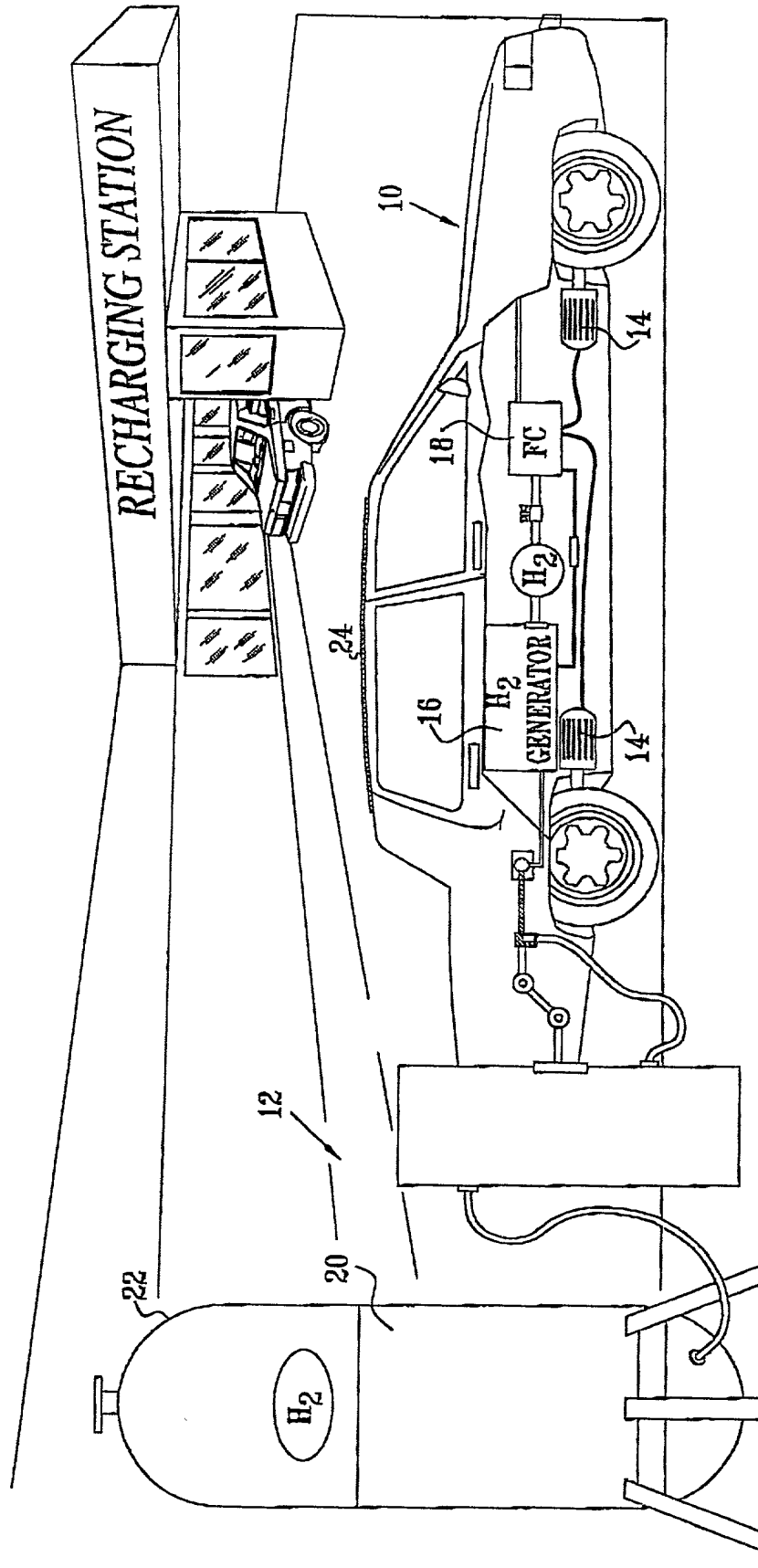
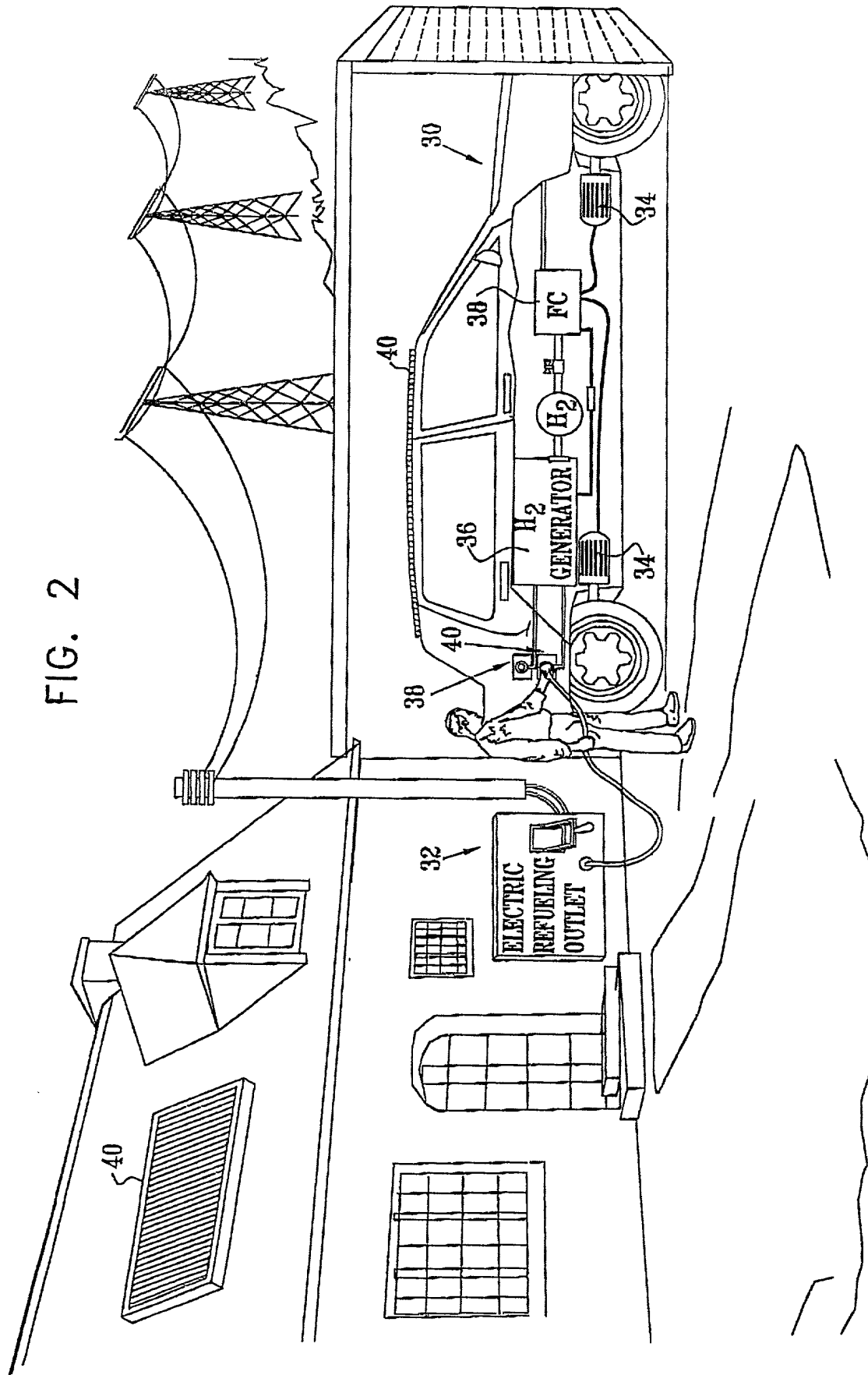
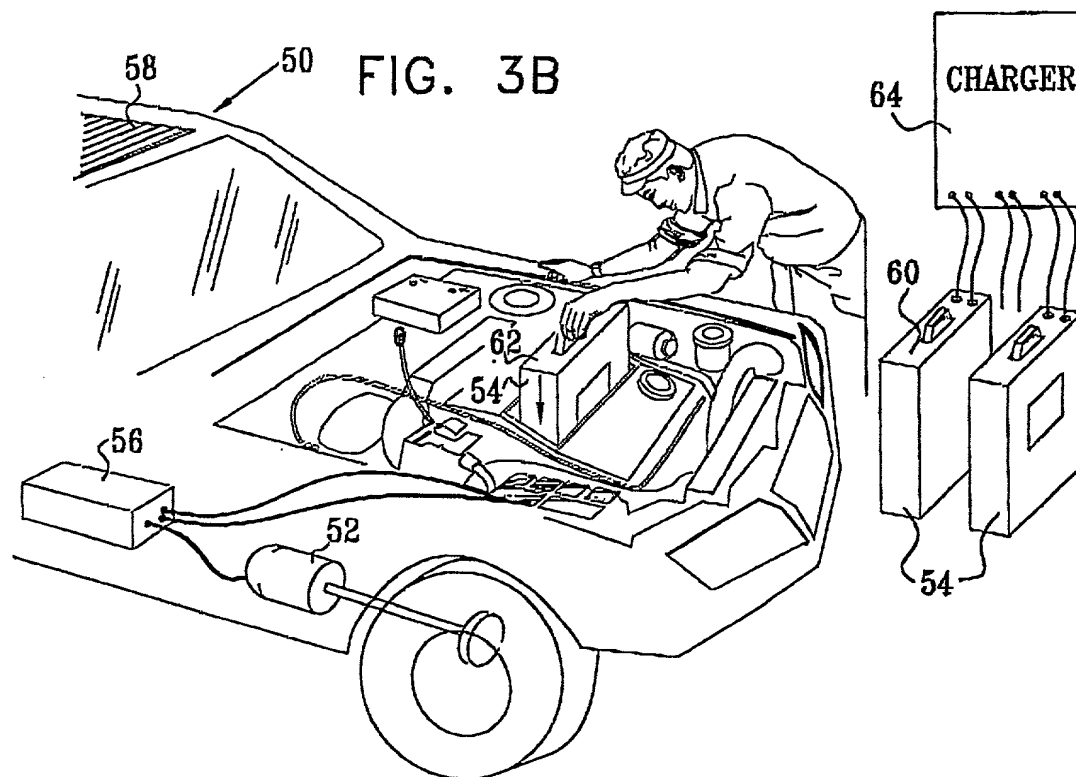
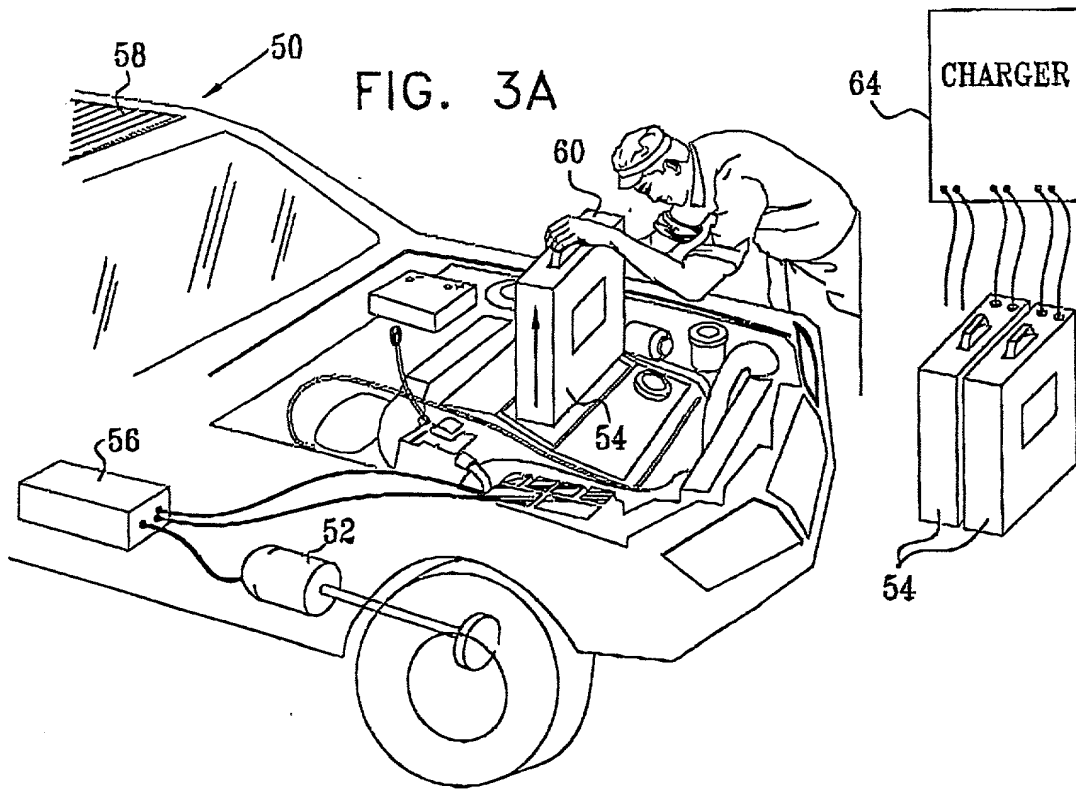
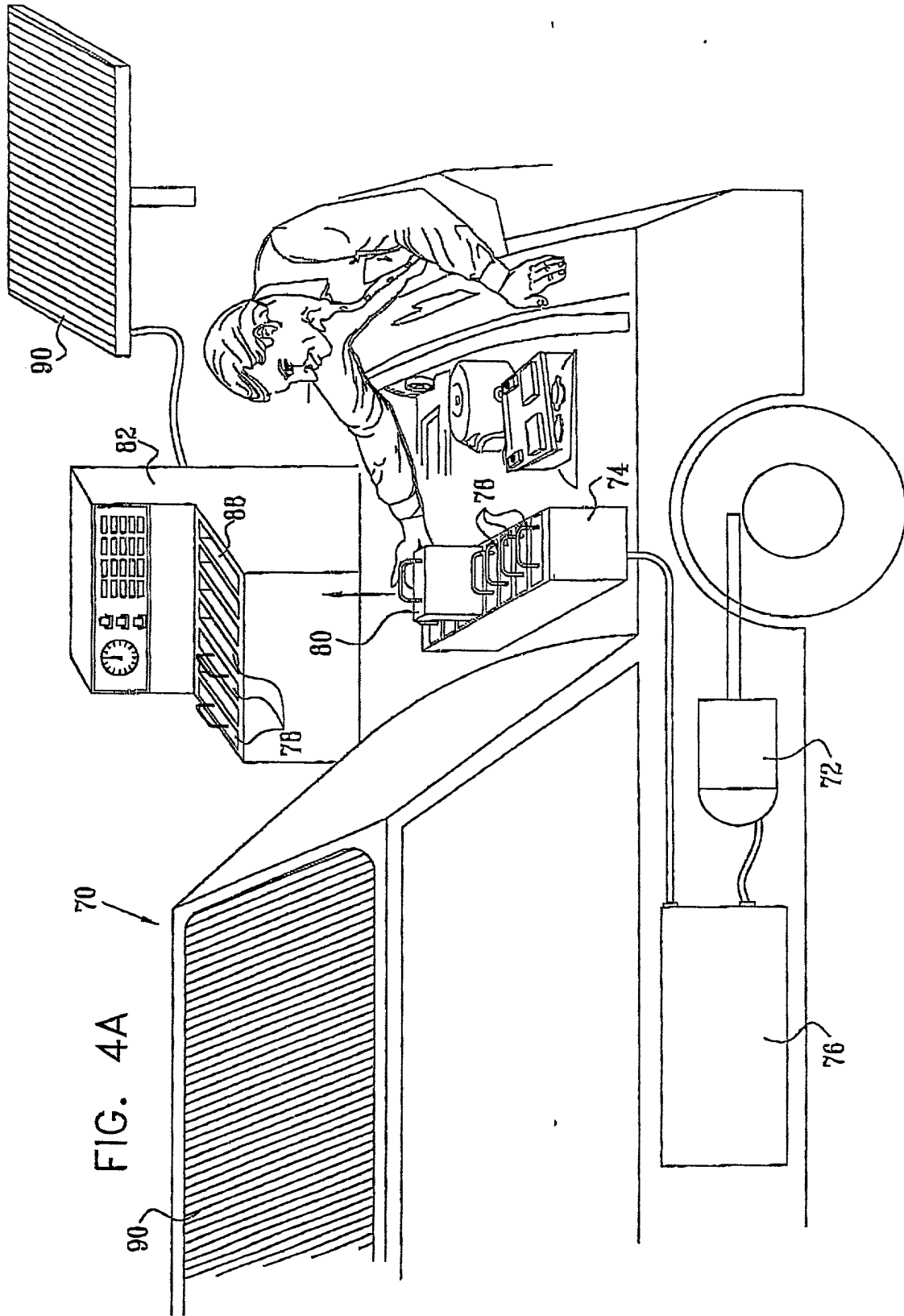


FIG. 2







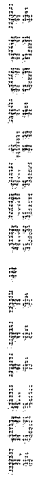


FIG. 4B / 70

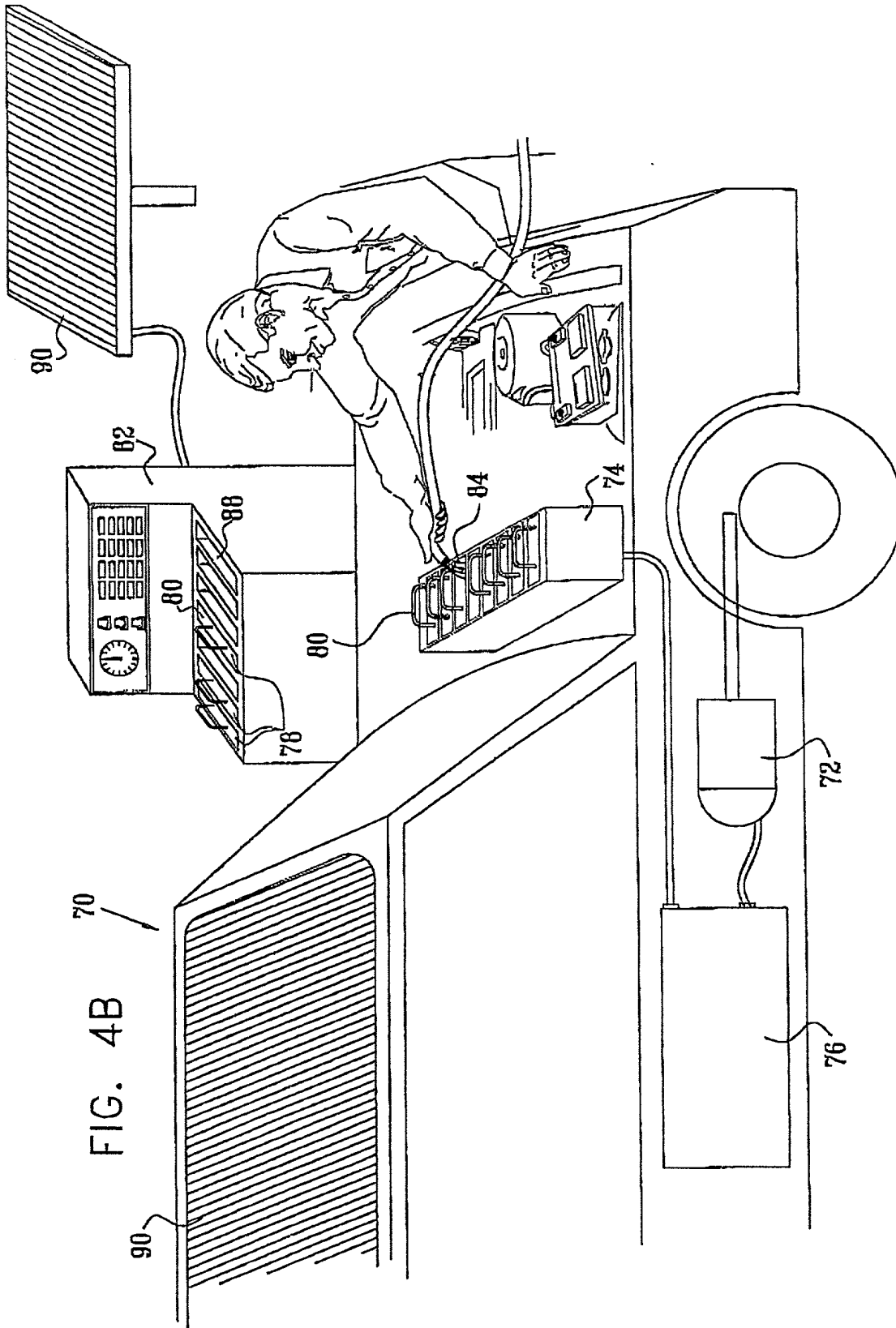


FIG. 4C

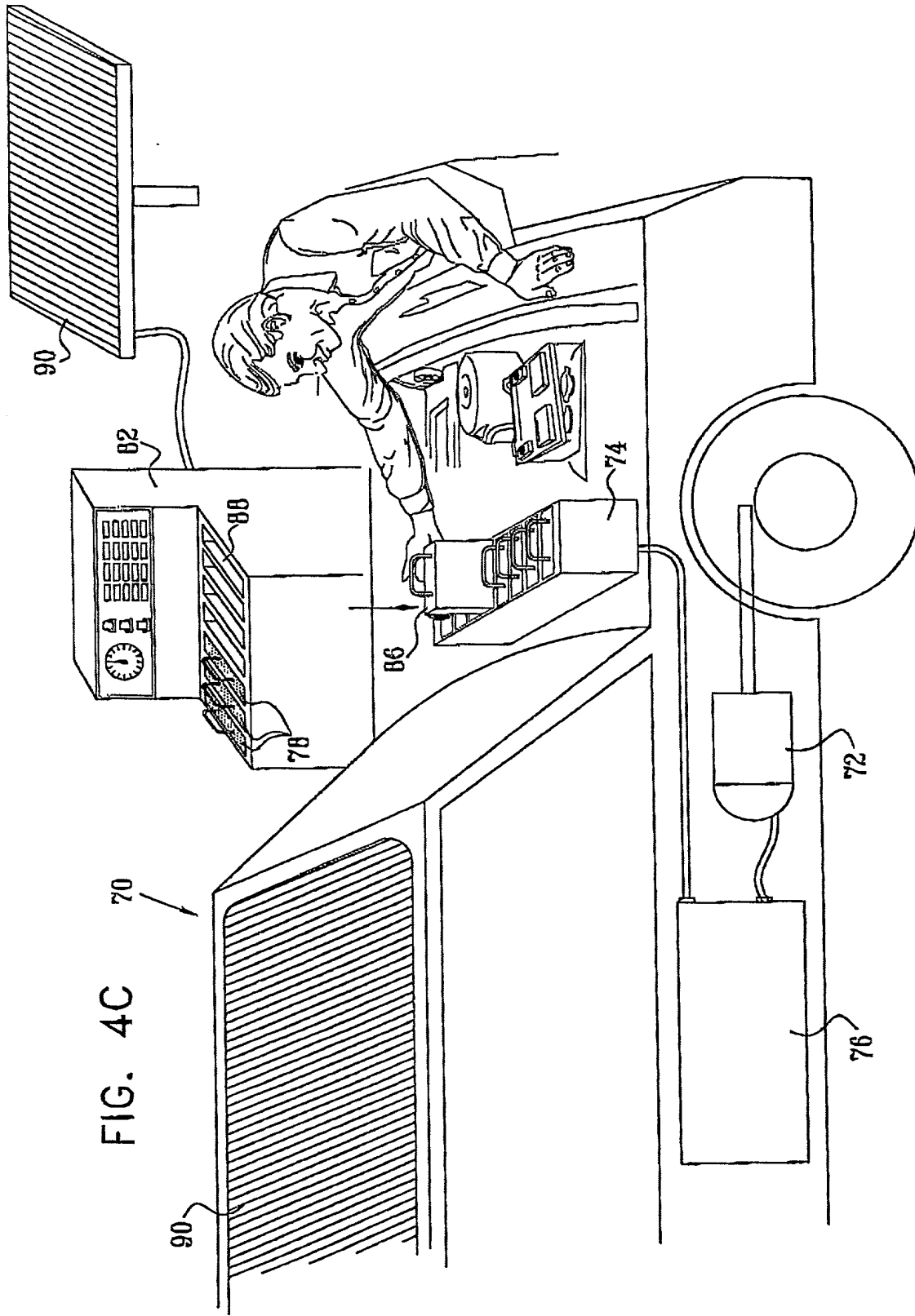


FIG. 5A

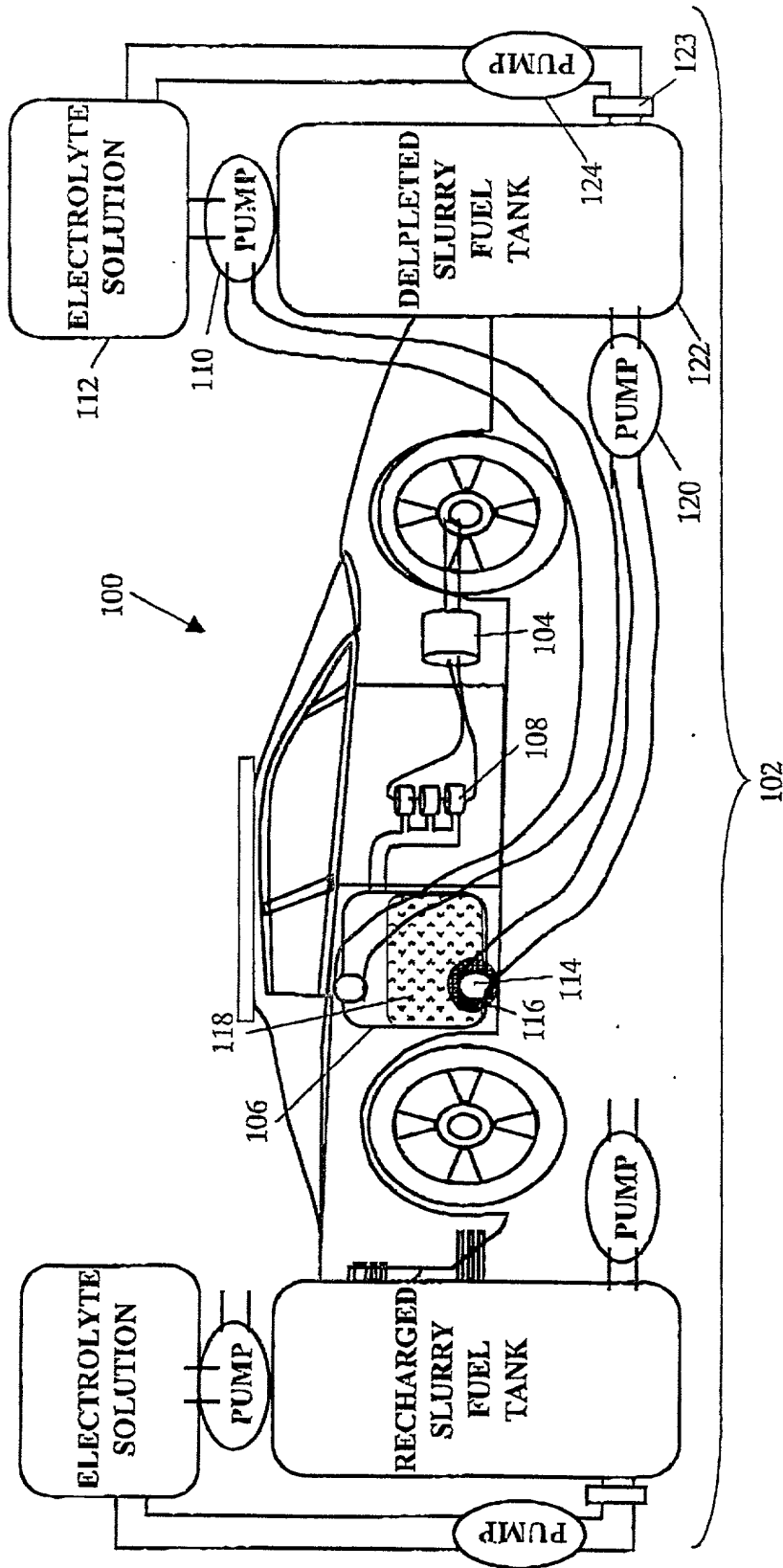


FIG. 5B

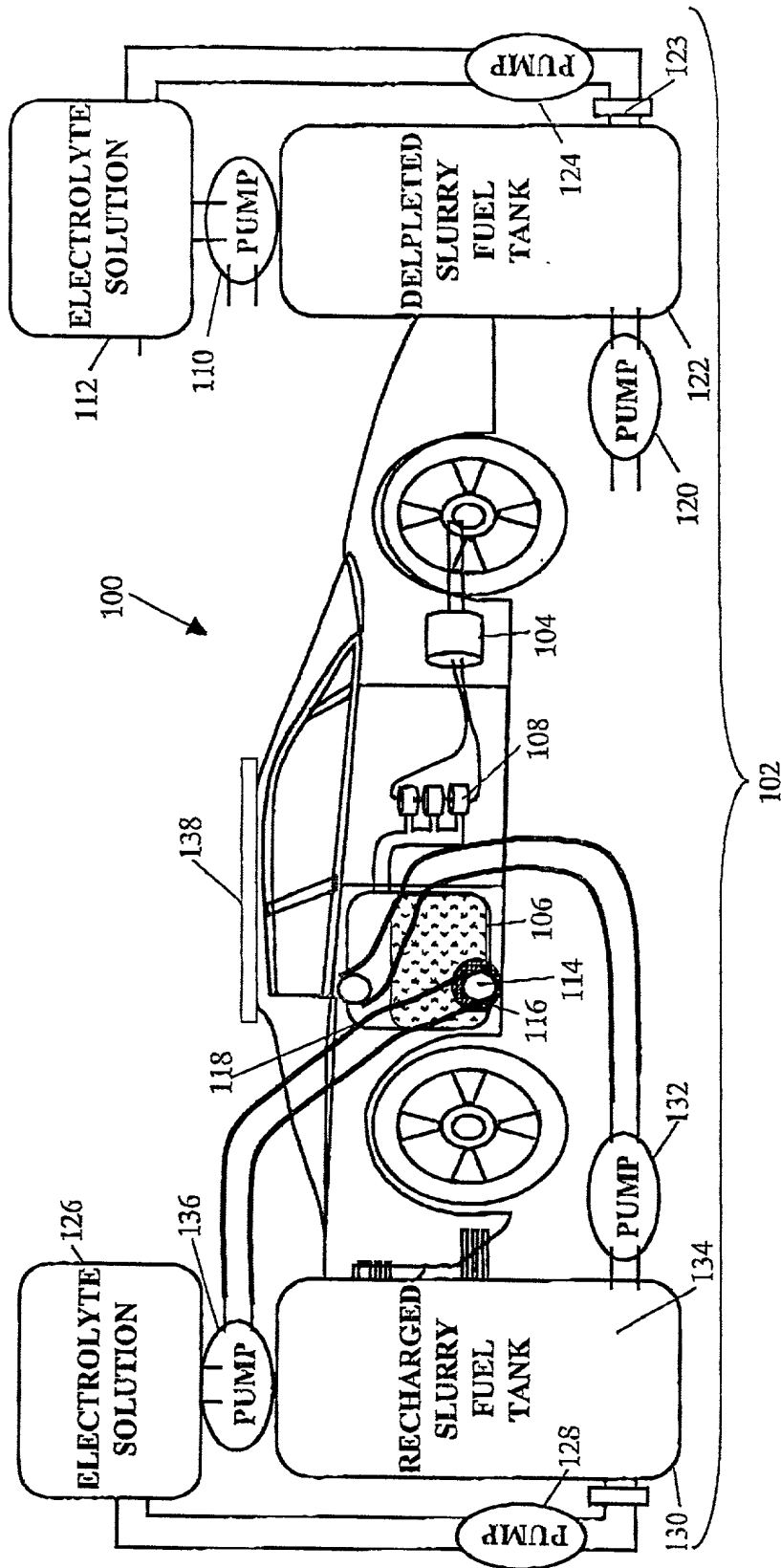




FIG. 6A

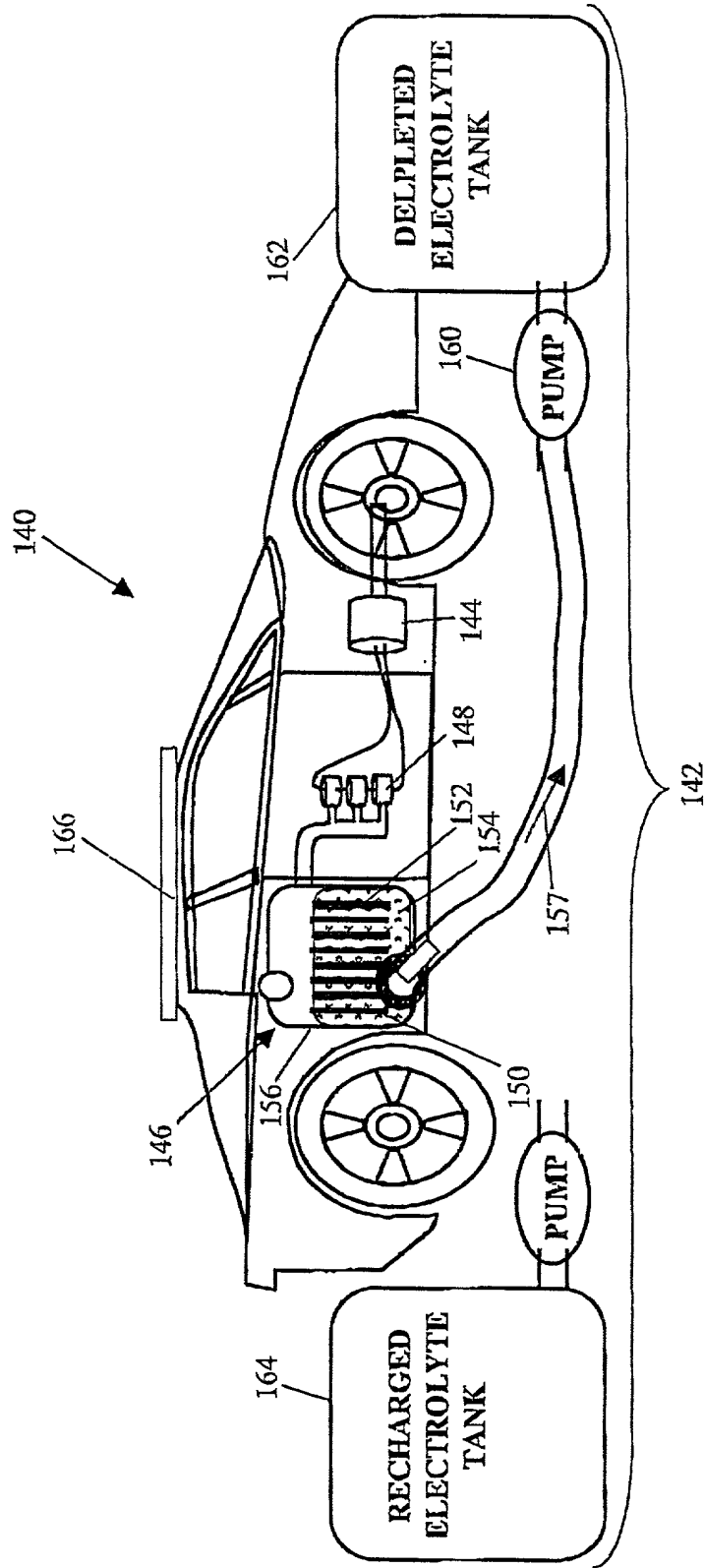
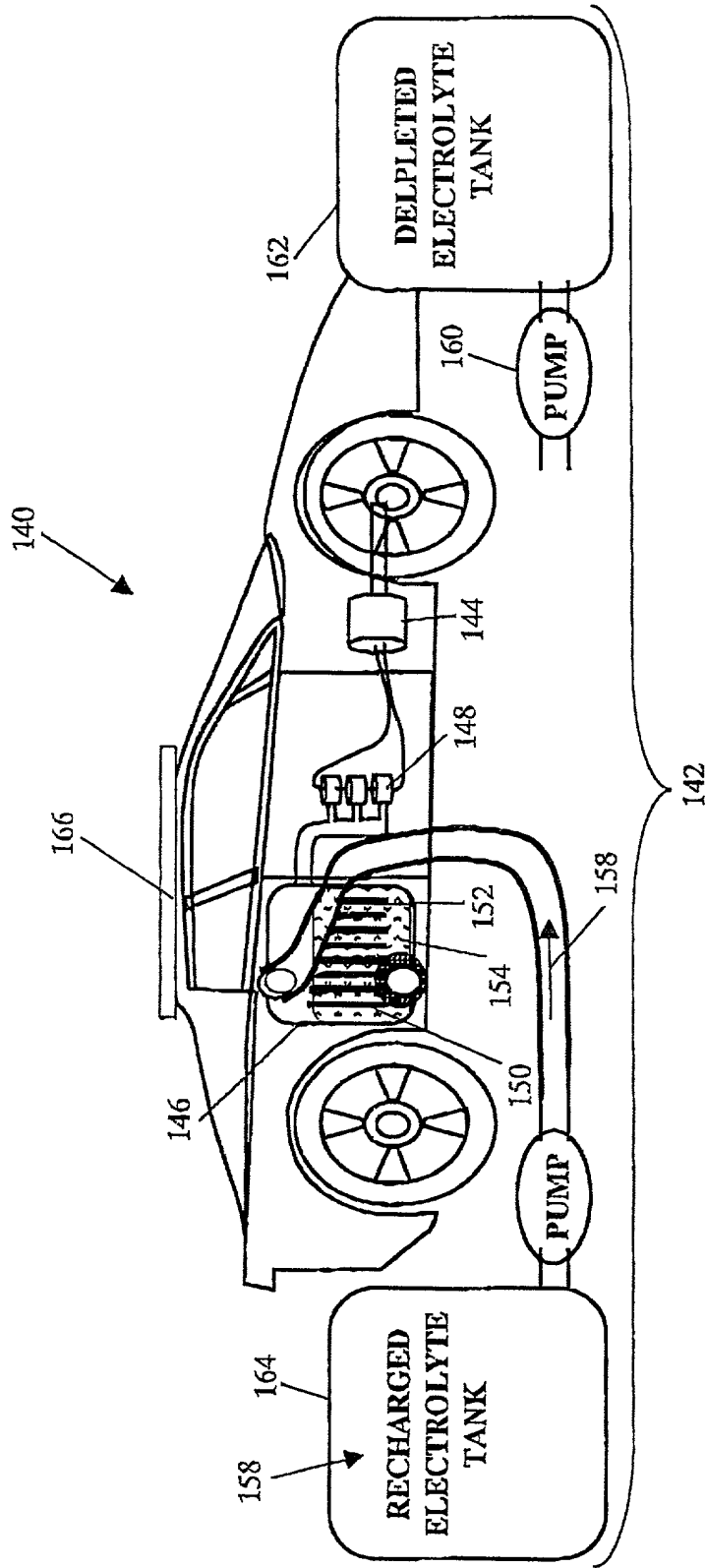


FIG. 6B



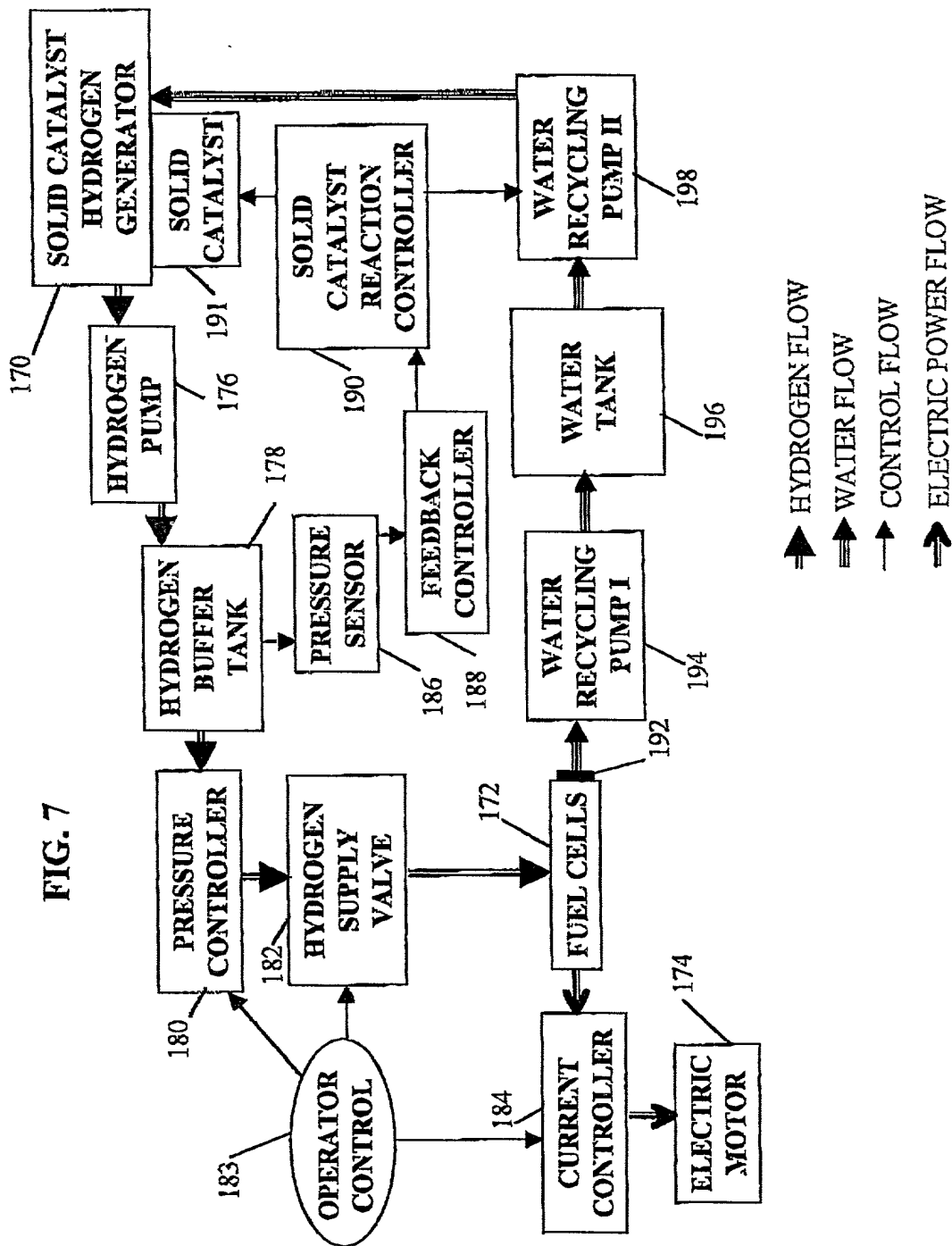


FIG. 8

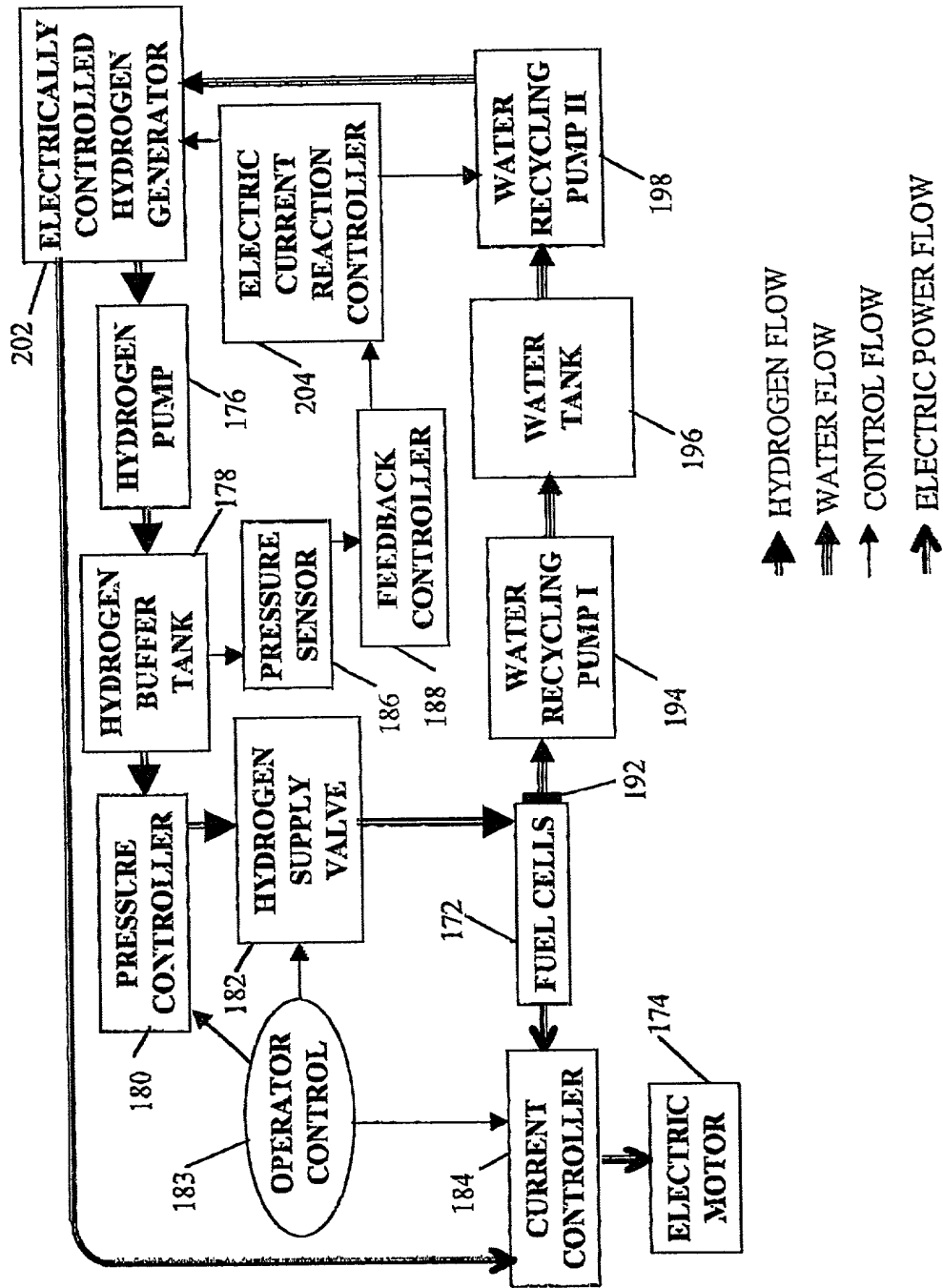


FIG. 9

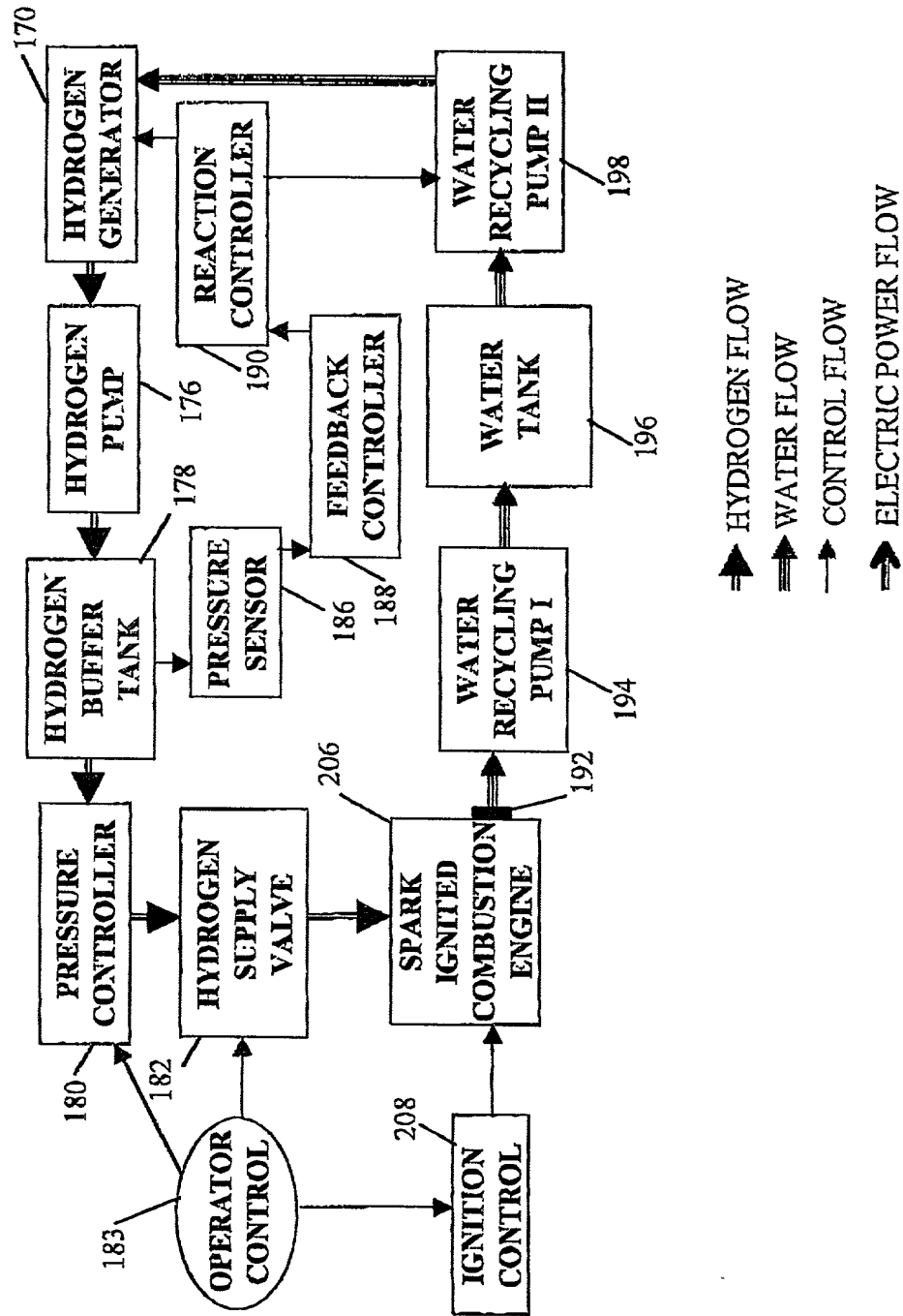


FIG. 10

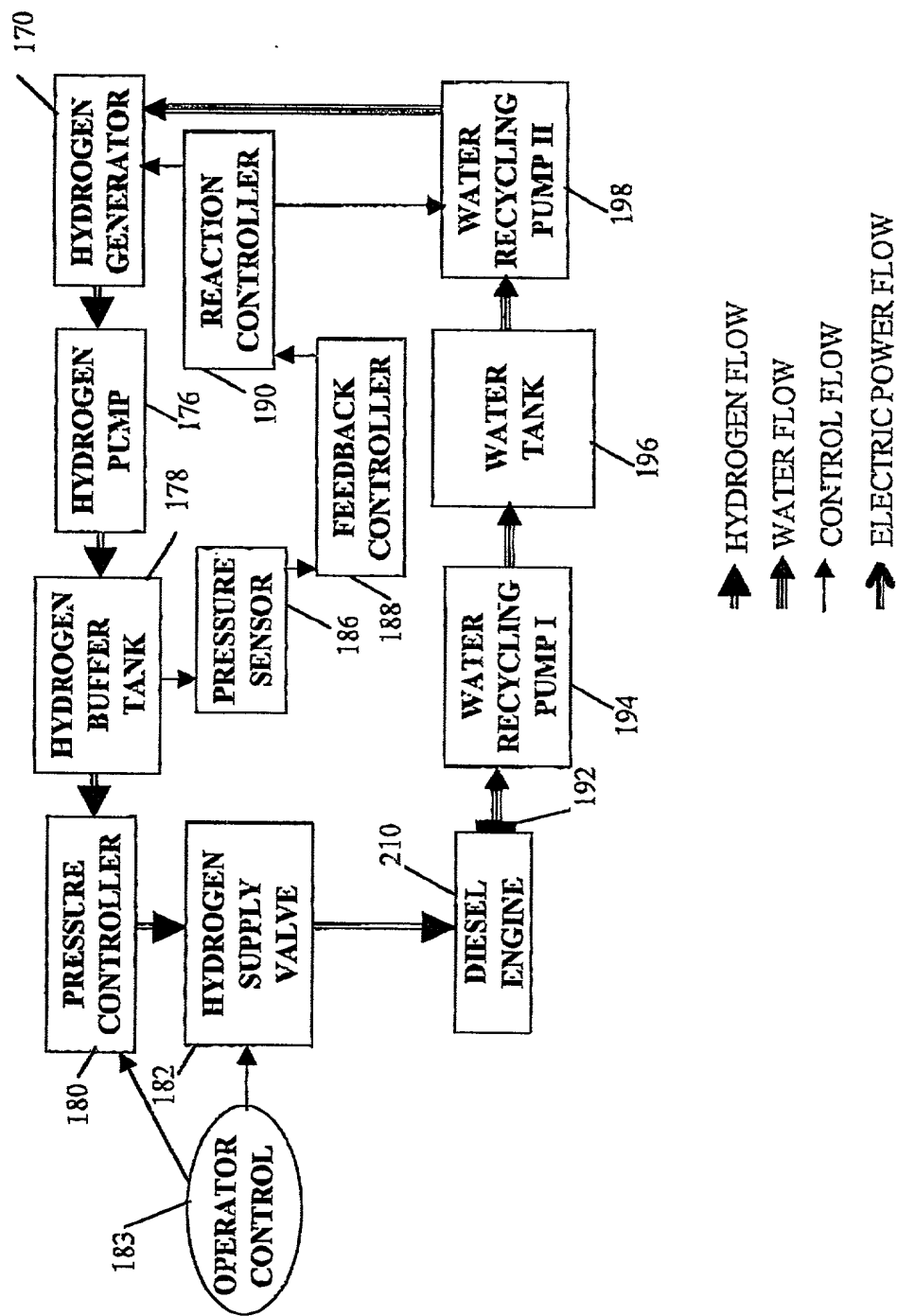


FIG. 11

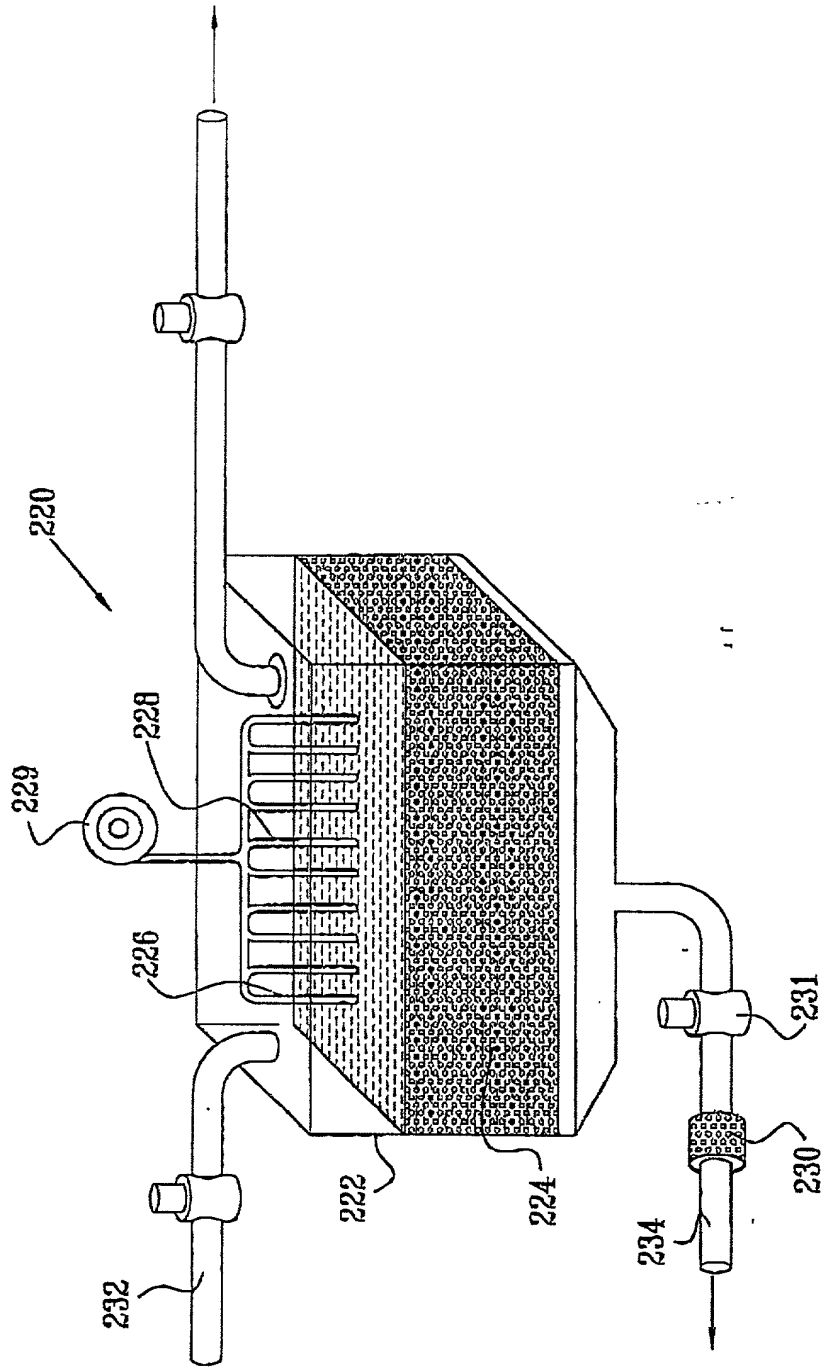


FIG. 12

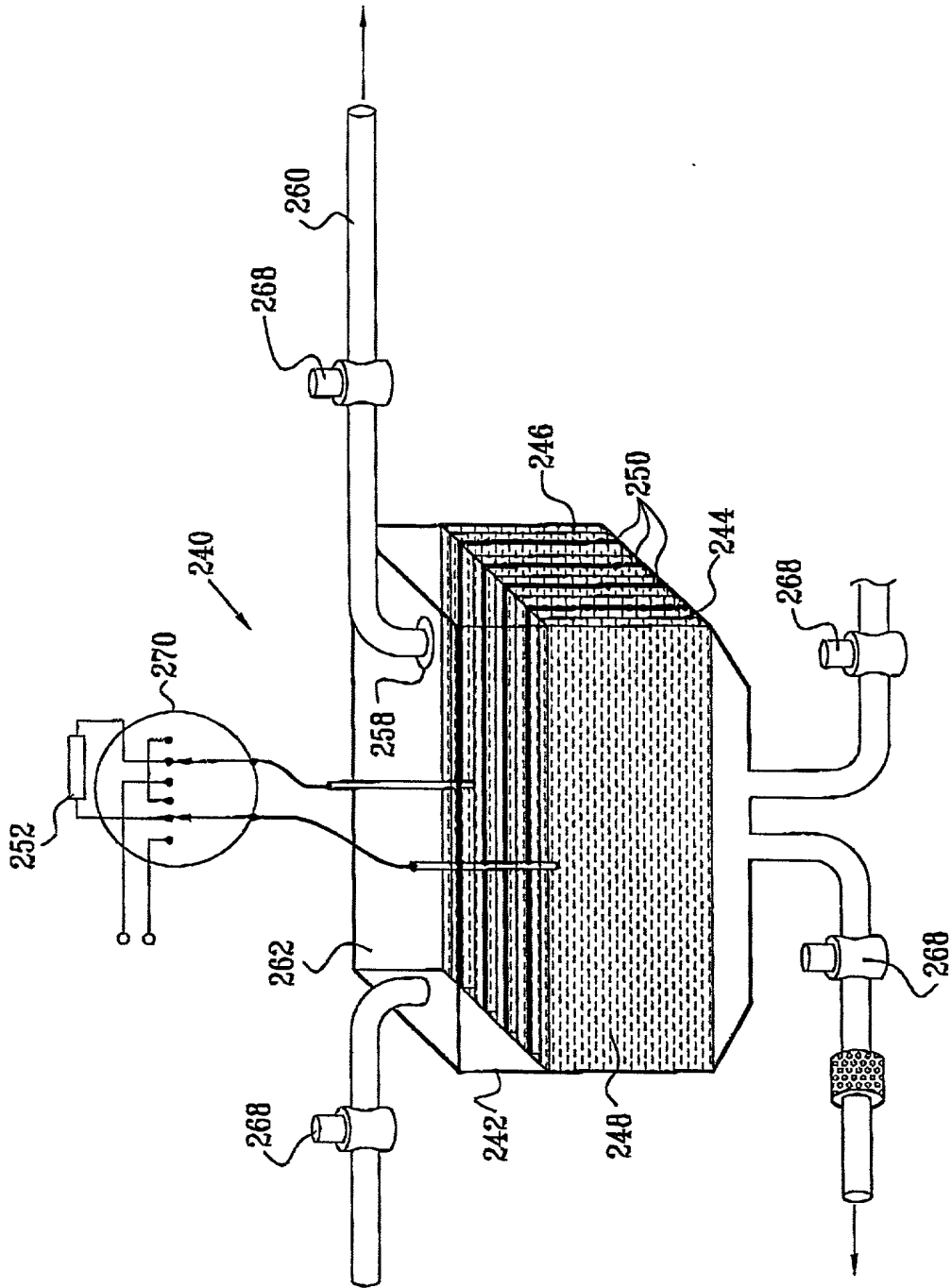




FIG. 13

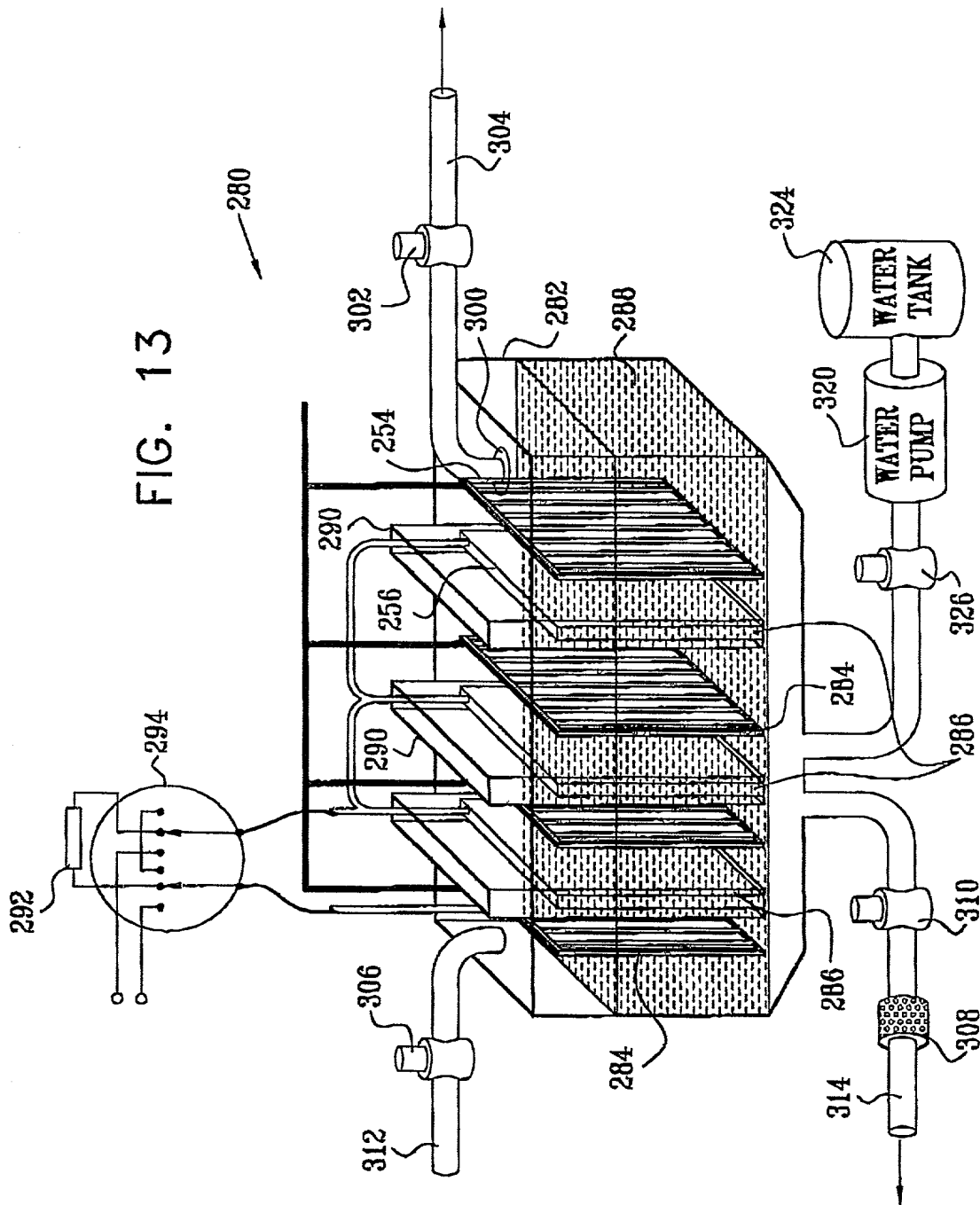


FIG. 14 is a schematic diagram of a system for testing a device. The system includes a power source 362, a switch 372, a meter 378, a device 370, a test chamber 364, a test chamber 366, a test chamber 368, a test chamber 390, a test chamber 396, a test chamber 398, a test chamber 400, and a test chamber 402.

360

FIG. 14

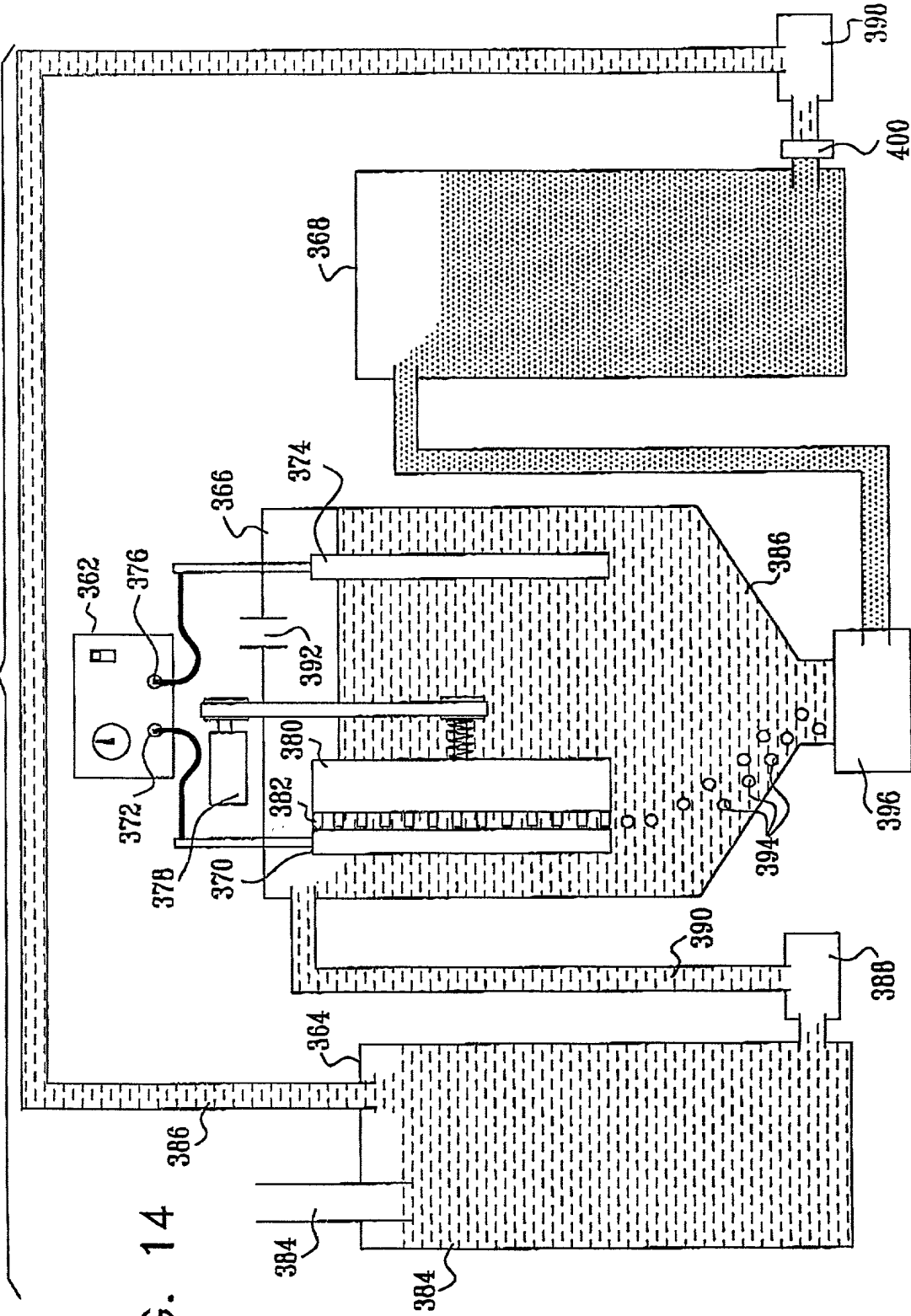


FIG. 15

